Useful TI references:

GPIO Pins and Alternate Functions - Page 650 – 651

Interrupt Register Map – 135

IRQ - 104

IPGES Usage

Port A

|  |  |
| --- | --- |
| Pin | Use |
| 0 |  |
| 1 |  |
| 2 | SSI0Clk – Solar Lights |
| 3 | SSI0Fss – Solar Lights |
| 4 | SSI0Rx – Solar Lights |
| 5 | SSI0Tx – Solar Lights |
| 6 |  |
| 7 |  |

Port B

|  |  |
| --- | --- |
| Pin | Use |
| 0 |  |
| 1 |  |
| 2 |  |
| 3 |  |
| 4 | PWM1 – Wind |
| 5 |  |
| 6 | PWM0 – Chopper Load |
| 7 | PWM0 – Chopper Load |

Port D

|  |  |
| --- | --- |
| Pin | Use |
| 0 |  |
| 1 |  |
| 2 | SYSCTL\_PERIPH\_GPIOD |
| 3 |  |
| 4 |  |
| 5 |  |
| 6 |  |
| 7 |  |

Port E

|  |  |
| --- | --- |
| Pin | Use |
| 0 | ADC Channel 3 | Sequencer2 | ADC0 |
| 1 | ADC Channel 2 | Sequencer2 | ADC0 |
| 2 | ADC Channel 1 | Sequencer2 | ADC0 |
| 3 | ADC Channel 0 | Sequencer2 | ADC0 |
| 4 |  |
| 5 |  |
| 6 |  |
| 7 |  |

Timer 2 -> Used to Trigger ADC